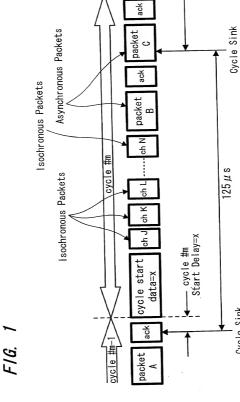
Cycle Sink



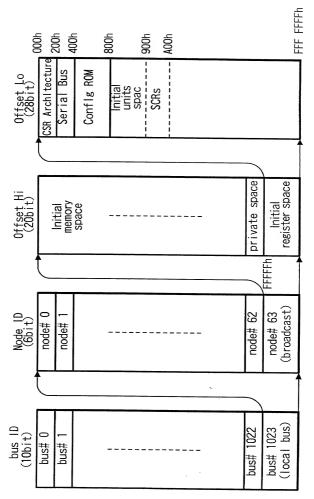
ch J

cycle start data=y

cycle #m+1

cycle #m+1 Start Delay=y

F16. 2



F16. 3

Offset	Designation	Function
4000	STATE_CLEAR	State and control data
004h	STATE_SET	Set state_clear bit
1800	NODE_IDs	Indicate node ID of 16 bits
00Ch	RESET_START	Start command reset
018h-01Ch	SPLIT_TIMEOUT	Specify maximum time of split
200h	CYCLE_TIME	Cycle time
210h	BUSY_TIMEOUT	Specify limit on retry
21Ch	BUS_MANAGER	Indicate ID of bus manager
220h	BANDWIDTH_AVAILABLE	Indicate band that can be assigned to isochronous communication
224h-228h	CHANNELS AVAILABLE	224h-228h CHANNELS AVAILABLE Indicate the state where the channels are used

FIG. 4

Info_length	info_length	crc_length	rom_crc_value
1		bus_info_blo	ck
		root_director	ry
	L	unit_directori	ies
	r	oot & unit lea	ves

F/G. 6

0001-1	0
900h	Output Master Plug Register
904h	Output Plug Control Register #0
:	Output Plug Control Register #1
	•
:	•
97Ch	Output Plug Control Register #30
980h	Input Master Plug Register
984h	Input Plug Control Register #0
988h	Input Plug Control Register #1
:	
:	•
9FCh	Input Plug Control Register #30

rom_crc_value _rec reserved Chip_ID_hi	CRC ndor_id bilities ld offset ory offset	Unit_spec_id unit_sw_version Optional.
rc_length	11111	length unit_spec_ unit_spec_ unit_sw_v
Company	Rootdirectory rootlength U3h UCh 8Dh D1h	unit_directory unit_directory_ 12h 13h
400h H	414h 418h 416h 428h 428h 428h	

F16. 74

	number of output plugs	5 (bit)
	reserved	3
	persistent extension field	_∞
	non-persistent extension field	_∞
	Broadcast channel base	9
UMPR	data rate capability	2

F/G, 78 opcr [n]

on-line

payload overhead ID data rate channel number point-to-point reserved

ဖ

10 (bit)

F16. 7C

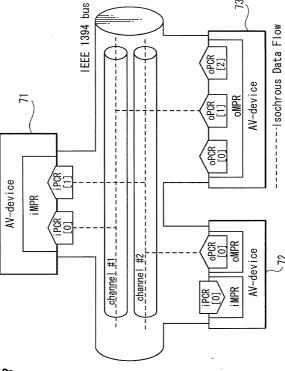
MPR

5 (bit)	င	_∞	8	9	2 2
number of input plugs	reserved	persistent extension field	non-persistent extension field	reserved	data rate capability

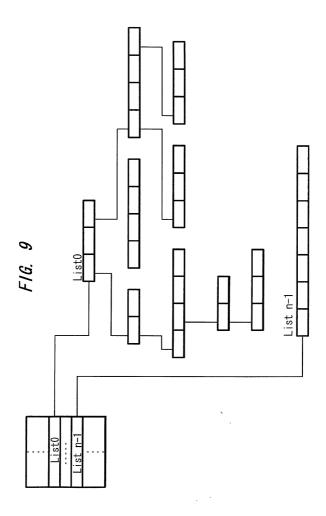
F16. 70

iPCR [n]

16 (bit)	9	2	9	_	
reserved	channel number	reserved	point-to-point connection counter	Broadcast connection counter	on-line



F16. 8



The Gene	ral Subunit Identifier Descriptor
address	contents
00 0016	descriptor length
00 0116	descriptor_rength
00 0216	generationID
00 0316	sizeoflistlD
00 0416	sizeofobjectID
00 0516	sizeofobjectposition
00 0616	number of root object lists(n)
00 0716	
00 0816	want object list id O
:	root_object_list_id_0
	:
:	root_object_list_id_n-1
	subunit_dependent_length
	subunitdependentinformation
:	manufacturerdependent`_!ength
	manufacturerdependent information

FIG. 11

generation	ID values
generationID	meaning
0016	Data structures and command sets as specified in the AV/C General Specification, version 3.0
all others	reserved for future specification

FIG. 12

List ID Value As	signment Ranges		
range of values	list definition		
000016-0FFF16	reserved		
100016-3FFF16	subunit-type dependent		
400016-FFFF16	reserved		
1 00016 -max list ID value	subunit-type dependent		

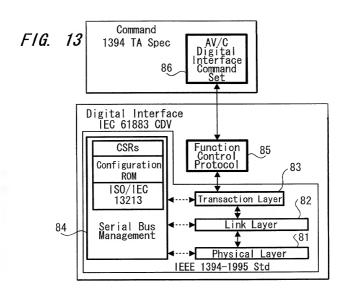
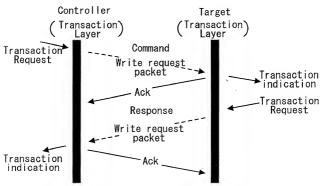
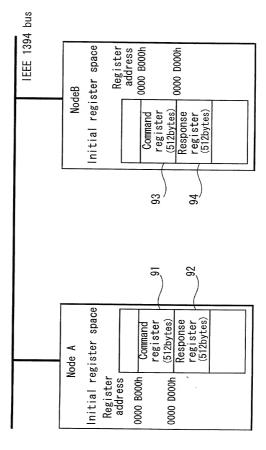


FIG. 14



F16. 15



F16. 16

Asynchronous Packet (Write Request for Data Block)

	packet	header			<i></i>	data	block	transmitted	3
destination ID ti ti rt tcode pri	source_ D destination_offset=	FCP_RESPONSE/FCP_COMMAND register	data_length extended_tcode	header_CRC	CTS= ctype/ subunit id opcode operand	Additional operands (if necessary)	padding (if necessary)	data_CRC	<pre>4 1 quad et=32bits</pre>
transmitted first									

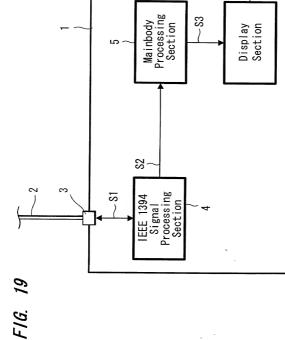
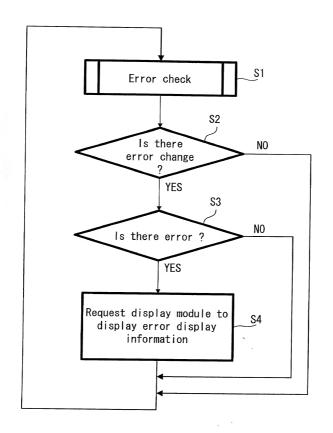
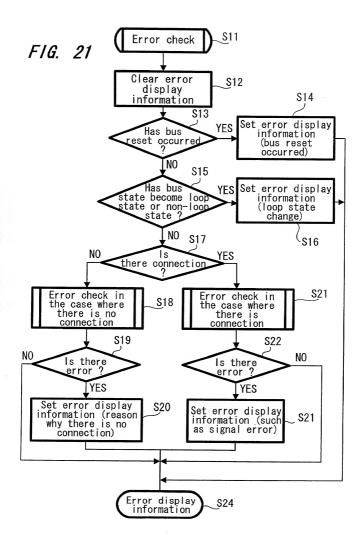
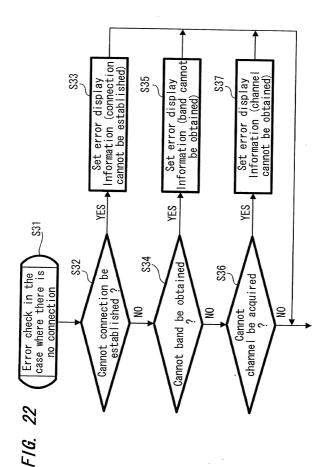


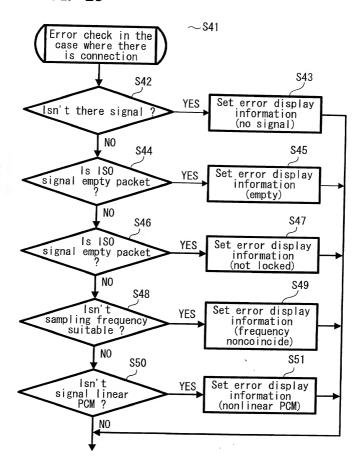
FIG. 20







F/G. 23



61

31 Connection failure of its own input 23Discrepancy between N bits(asynchronous) and rate control protocol 12 Connection failure of output plug of opposite device side Channel is fully occupied time of output The format is not IEC958 format he sampling frequency is not suitable 13 Band is deficient at the of input to the lime of input of consistency of the of output output. Band is deficient at the time of outpur The signal is not linear PCM The signal is unlocked There are no signal Display message 22 15 25 26 33 13 24 21 f standard Ished incs and it cannot The case where there are not input signals at all during selection of a connection device Since bus is full of signals output or input cannot be conducted SIR has 63 formed Links and it cannot have more Links. Bus reset has occurred (for example in the case where new device is connected) The case where a different format (signal where cannot be reproduced) it detected -oop has been formed by cable connection Temperature within the device is rising Speaker terminal is short-circuited Selected device is not connected he case where the signal clock is out of values and the PLL lock is not estab 62 ncs The selected is conduction 63 cope with more F1G. 24 C78:11(At the time of device selection) C78:12 (TUNER, ANALOG) Error code number example) C78:22.22 C78:22, 23 C78:22.25 C78:22.26 C78:15, 13 C78:15,14 C78:15,15 C78:15,33 C78:31 C78:04 C78:03 C78:00 060:01 80:090 C60:13

